

Fig. 1A (Prior Art)

004080" 280E3960

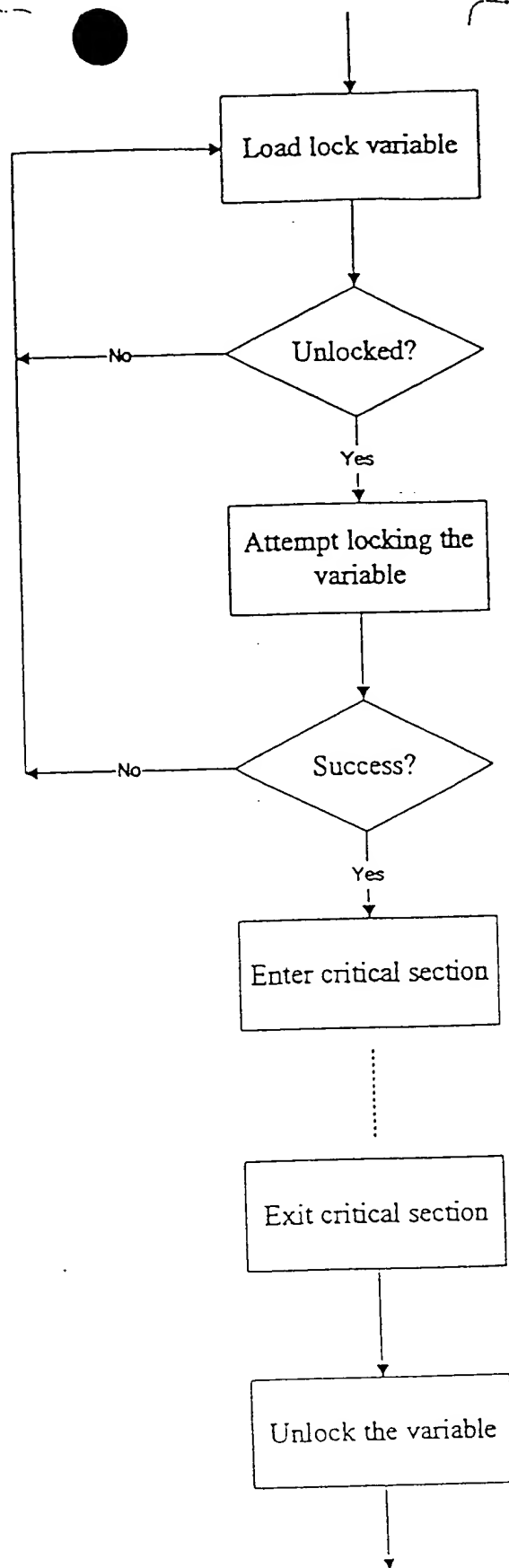
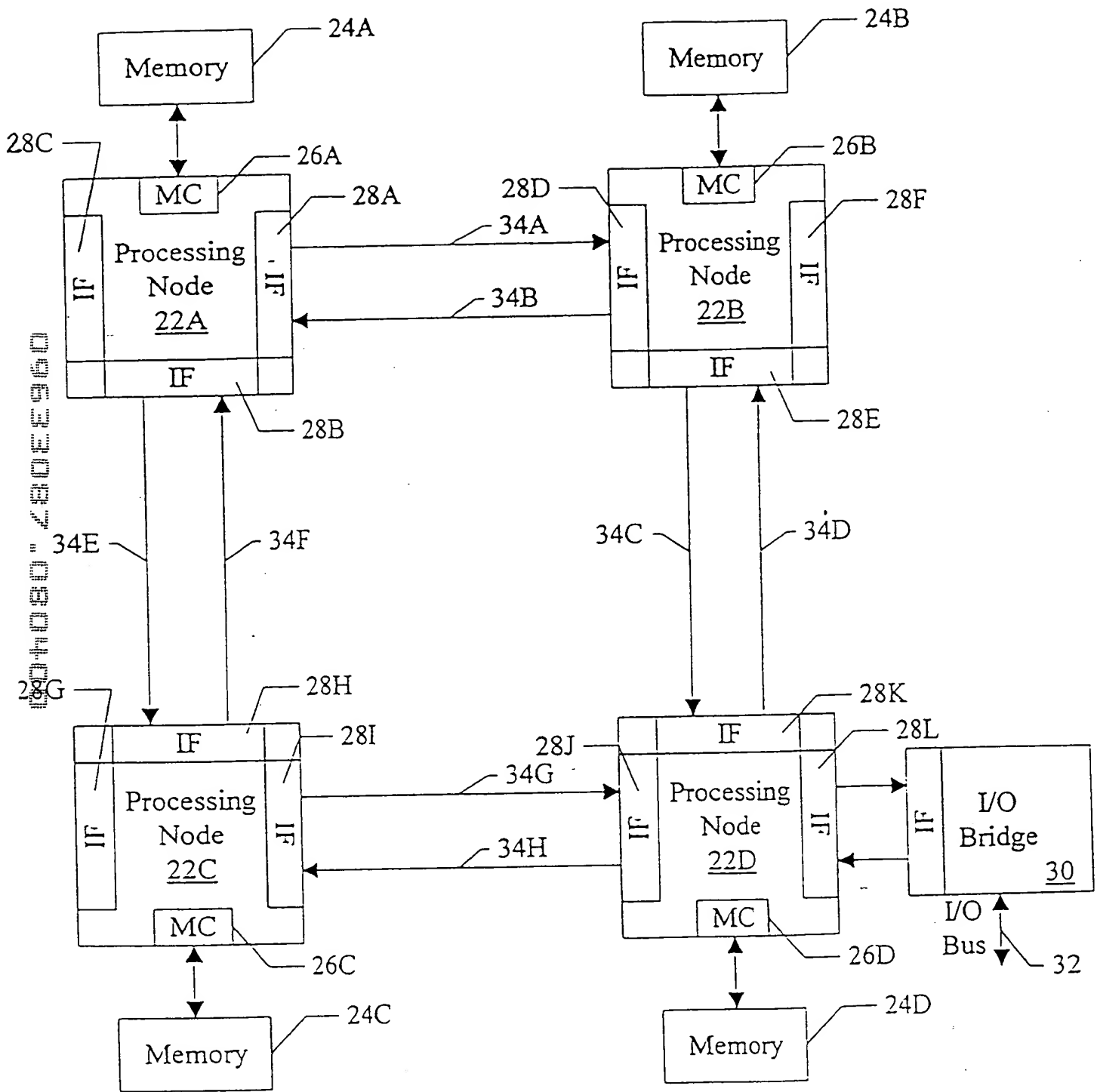


Fig. 1B (Prior Art)



20

Fig. 2A

004080" / 80E960

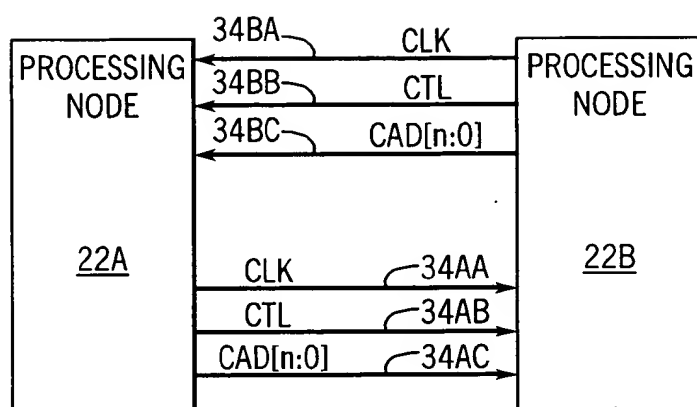


FIG. 2B

004080" 08EE960

Bit Time	7	6	5	4	3	2	1	0
0	Command-Specific		CMD[5:0]					
1	Command-Specific							
2	Command-Specific							
3	Command-Specific							

38 ↗

FIG. 3A

Bit Time	7	6	5	4	3	2	1	0
0	Src Unit [1:0]		CMD[5:0]					
1	Command-Specific					SrcNode[2:0]		
2	Command-Specific							
3	Addr[7:2]							
4	Addr[15:8]							
5	Addr[23:16]							
6	Addr[31:24]							
7	Addr[39:32]							

40 ↗

FIG. 3B

004030" 280E950

Bit Time	7	6	5	4	3	2	1	0
0	Command-Specific		CMD[5:0]					
1	DestNode[2:0]			Dest Unit[1:0]		Command-Specific		
2	Command-Specific							
3	Command-Specific							

42

FIG. 4A

Bit Time	7	6	5	4	3	2	1	0
0	Data[7:0]							
1	Data[15:8]							
2	Data[23:16]							
3	Data[31:24]							
4	Data[39:32]							
5	Data[47:40]							
6	Data[55:48]							
7	Data[63:56]							

46

FIG. 4B

004080" / 80EE960

Bit Time	7	6	5	4	3	2	1	0
0	Mask[7:0]							
1	Mask[15:8]							
2	Mask[23:16]							
3	Mask[31:24]							
4	Data[7:0]							
5	Data[15:8]							
6	Data[23:16]							
7	Data[31:24]							
8+	Packet may contain up to eight double words of data							

50

FIG. 5

<u>CMD Code</u>	<u>Command</u>	<u>Packet Type</u>
000000	Nop	Info
000001	VicBlk	Request / Address / Data
000010	Reserved	—
000011	ValidateBlk	Request / Address
000100	RdBlk	Request / Address
000101	RdBlkS	Request / Address
000110	RdBlkMod	Request / Address
000111	ChangeToDirty	Request / Address
x01xxx	Write(Sized)	Request / Address / Data
01xxxx	Read(Sized)	Request / Address
100xxx	Reserved	—
110000	RdResponse	Response / Data
110001	ProbeResp	Response
110010	TgtStart	Response
110011	TgtDone	Response
110100	SrcDone	Response
110101	MemCancel	Response
11011x	Reserved	—
11100x	Probe	Request / Address
11101x	Broadcast	Request / Address
11110x	Reserved	—
111110	Reserved	—
111111	Sync	Info

FIG. 6

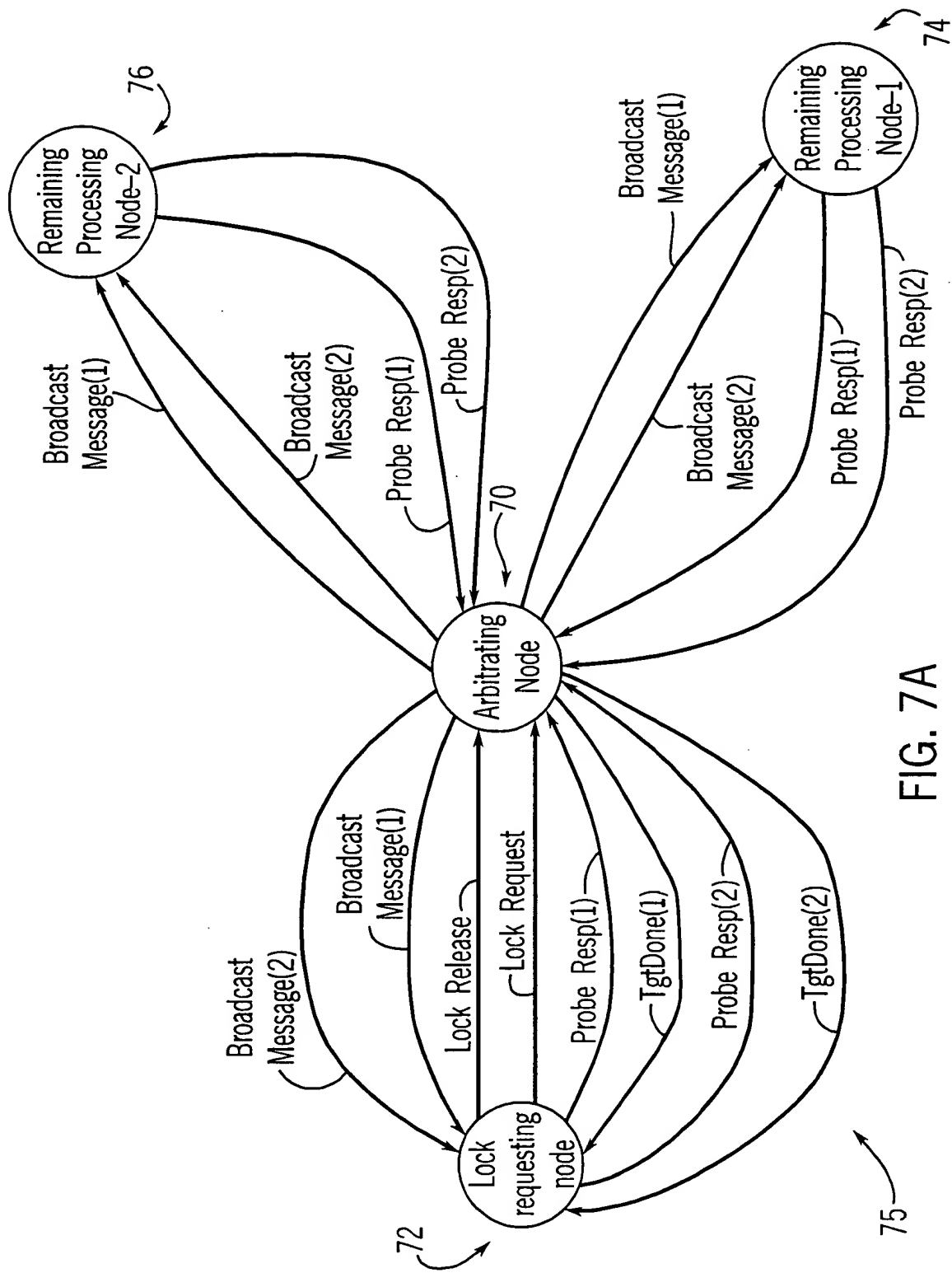
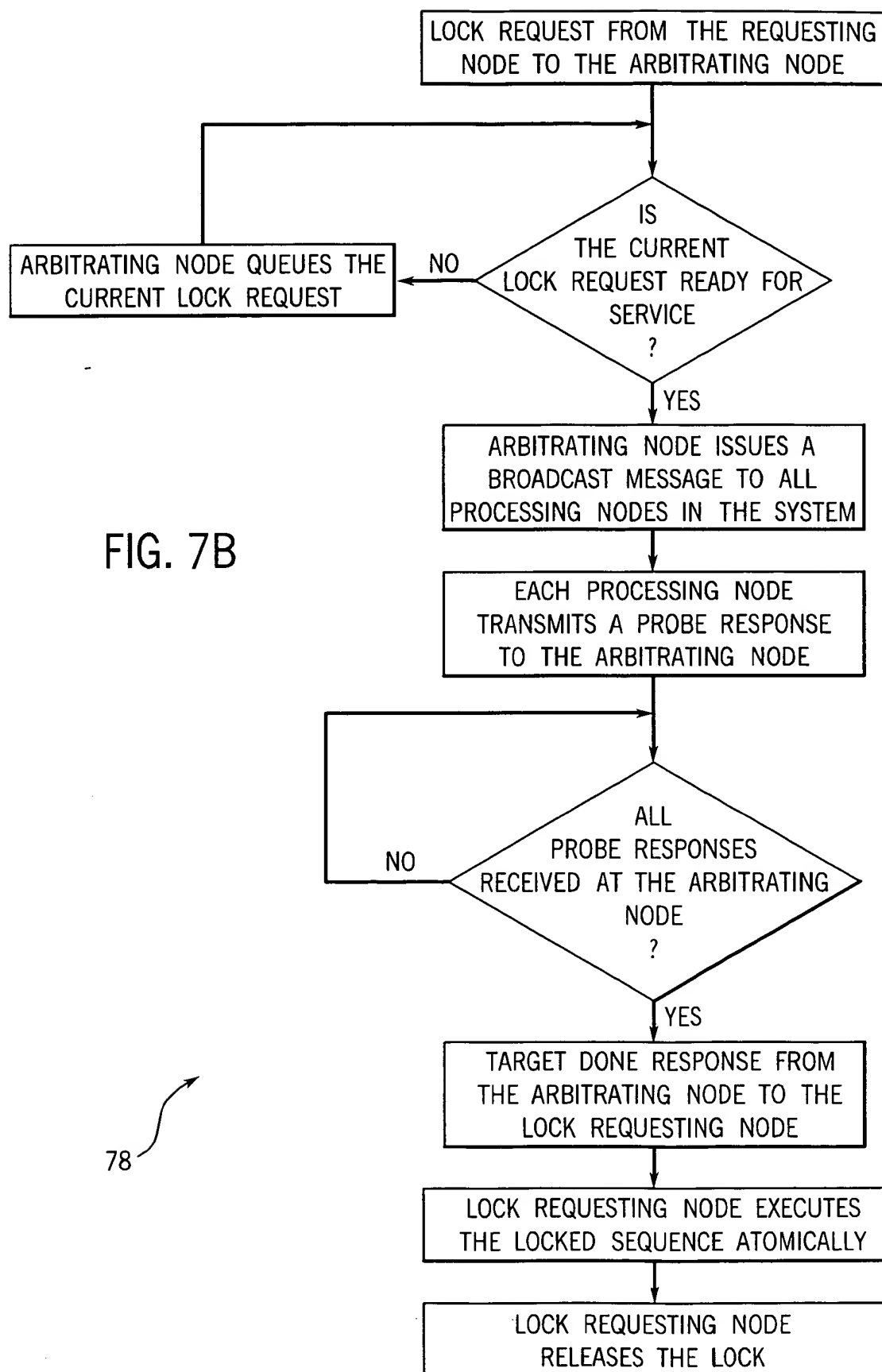


FIG. 7A



004080" 280EE960

Bit Time	7	6	5	4	3	2	1	0
0	SrcUnit [1:0]		CMD[5:0]					
1	DestNode [2:0]			DestUnit [1:0]		SrcNode [2:0]		
2	Mask / Count [1:0]		Compat	SrcTag[4:0]				
3	Addr[7:2]					Lock	Mask / Count[3:2]	
4	Addr[15:8]							
5	Addr[23:16]							
6	Addr[31:24]							
7	Addr[39:32]							

80

FIG. 8A

004030" 030400 096308" 030400

Bit Time	7	6	5	4	3	2	1	0
0	SrcUnit [1:0]		CMD[5:0]					
1	TgtNode [2:0]			TgtUnit [1:0]		SrcNode [2:0]		
2	Rsv			SrcTag[4:0]				
3	Addr[7:2]						Reserved	
4	Addr[15:8]							
5	Addr[23:16]							
6	Addr[31:24]							
7	Addr[39:32]							

82

FIG. 8B

Bit Time	7	6	5	4	3	2	1	0
0	SrcUnit [1:0]		CMD[5:0]					
1	DestNode[2:0]			DestUnit [1:0]		SrcNode [2:0]		
2	Reserved		Error	SrcTag[4:0]				
3	Shared	Reserved						

84

FIG. 8C

004090" 280E960

Bit Time	7	6	5	4	3	2	1	0
0	SrcUnit [1:0]		CMD[5:0]					
1	DestNode[2:0]			DestUnit [1:0]		SrcNode [2:0]		
2	Reserved		Error	SrcTag[4:0]				
3	Reserved							

85

FIG. 8D